

EDA Group

Massimo Poncino

Dipartimento di Automatica e Informatica





the EDA group

- Electronic Design Automation
 - 6 Faculty members
 - Enrico Macii
 - Massimo Poncino
 - Alberto Macii
 - Andrea Acquaviva
 - Elisa Ficarra
 - Andrea Calimera
 - 4 post-doc researchers
 - ~10+ Ph.D. students & Research Assistants
- Three main areas of research:
 - *EDA*
 - *Technologies for Smart Cities*
 - *Bioinformatics*



EDA in the EDA group

- Historical research area of the group
 - Are of major research achievements
- Summary in a nutshell:
design automation of energy-efficient systems

Recent Research Topics (1)

- CAD for electrical energy systems (EES)

- Battery modeling

- Battery modeling (fast and accurate)
 - Battery modeling (capacity, efficiency, etc.)
 - Macromodels

- Hybrid EES

- Optimal control
 - Energy storage /recycling
 - Interaction and power
 - Thermal management
 - Thermal storage devices

- Smart battery chargers

- Non-standard algorithms/policies for fast charge

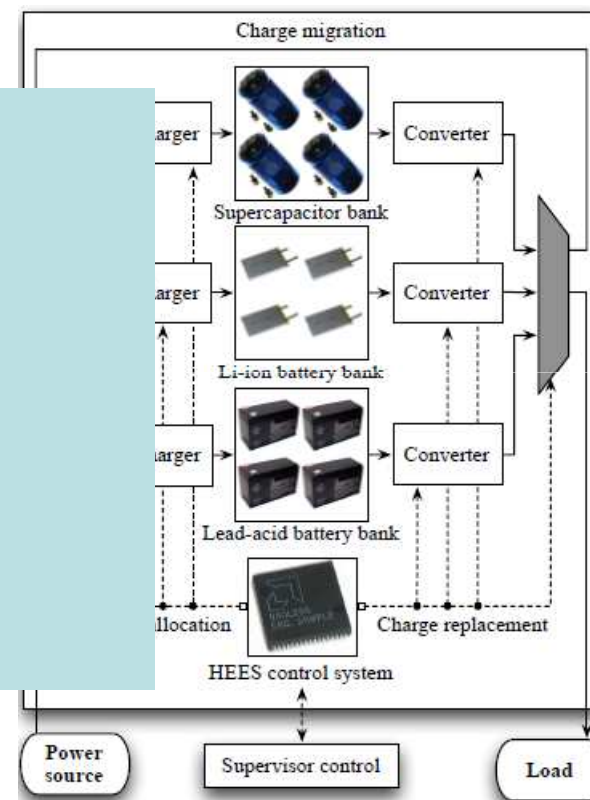
People:

Alberto Macii

Sara Vinco

Alberto Bocca

Yukai Chen



Recent Research Topics (2)

• CAD for electrical energy systems (EES)

– General Cyber-Physical Energy Systems

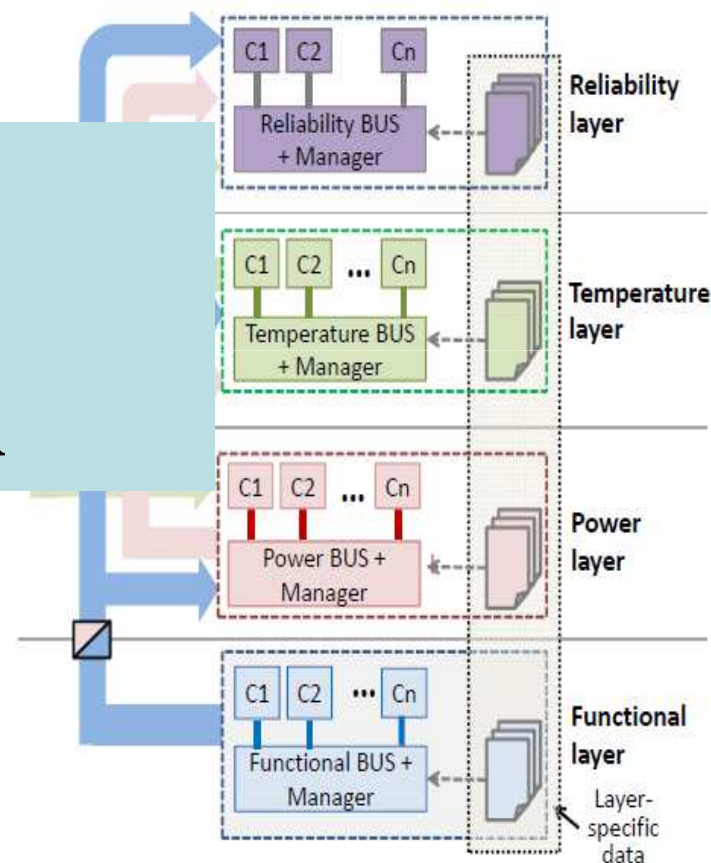
– Open-source, extended SystemC/AMS+IP-X framework

– Implementation of

– Extension to extra-f

- Aging
- Temperature
- Operational cost

People:
Sara Vinco
Yukai Chen



Recent Research Topics (3)

- Approximations for energy-efficient systems

- Computation

- Architectural and circuit-level techniques for optimal accuracy/power tradeoffs

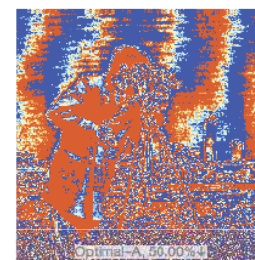
- Data

- Image ma

People:

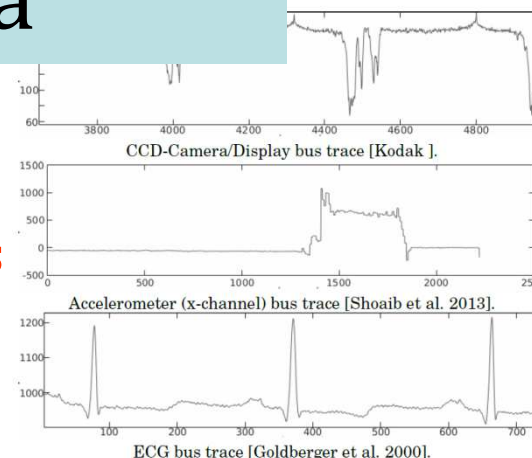
Daniele Jahier Pagliari

Andrea Calimera



- Sensing

- Serial encodings for sensor interfaces



Recent Research Topics (4)

- CAD for smart systems/devices

- Integration-aware synthesis in the digital domain

- Modeling of non-functional properties
- Automated synthesis of self-detecting digital IP based on the abstraction for TLM simulation

- Modeling and devices and system co-simulation (fits into #2 and #3)

- Abstract modeling of devices and systems
- Abstract modeling of devices (based on TLM)

People:

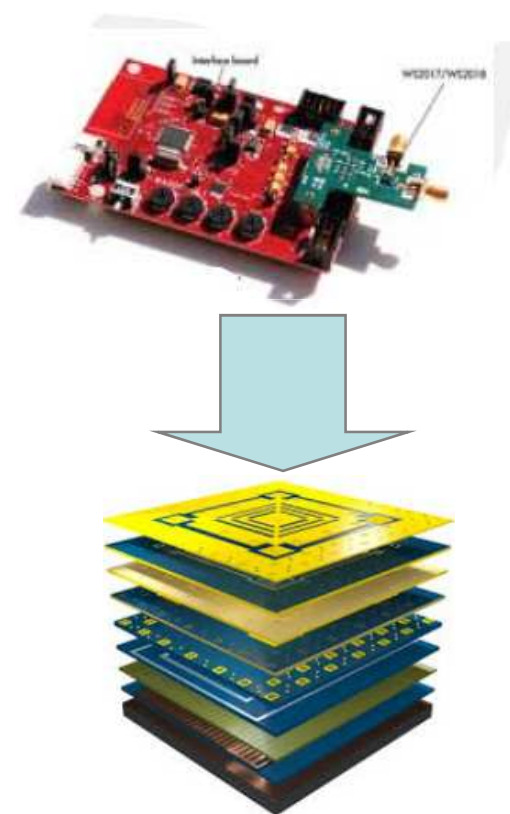
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Other Research Topics

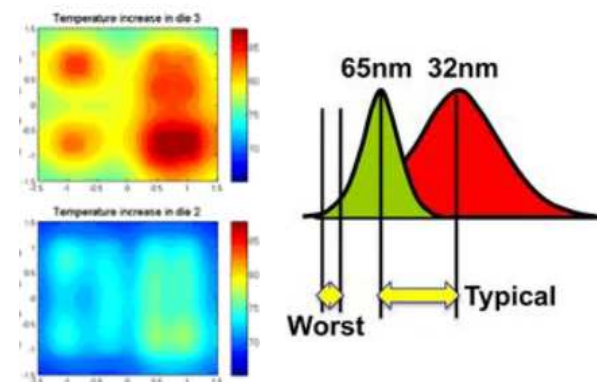
- **EDA for energy-efficient systems**

- **Leakage management in DSM designs**
 - Power-gating of standard-cell designs and memories
- **Energy-efficient chip multicore**
 - Energy-efficient memory sub-system design
 - Technology implications of voltage/frequency scaling



- **EDA for “indirect metrics”**

- **Thermal-aware design & architectures**
 - Adaptive compensation of thermal gradients
 - Leakage/Thermal-aware co-synthesis
 - Thermal-aware design of clock trees
- **Variation-tolerant design techniques**
 - Latency/skew control
 - Use of power management knobs (power gating) to compensate variations



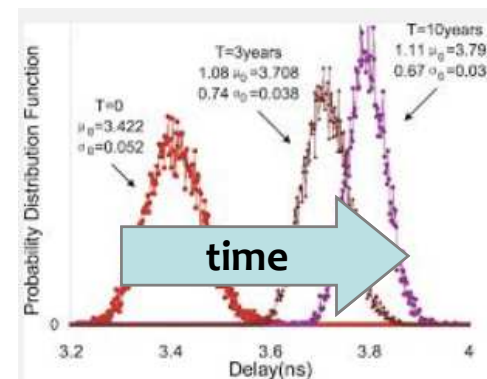
Other Research Topics

- Aging management in circuits and systems

- **NBTI-aware memory architectures**

- Partitioned memory architectures for improved aging
- Time-varying cache indexing
- Aging-driven graceful degradation of performance in memories

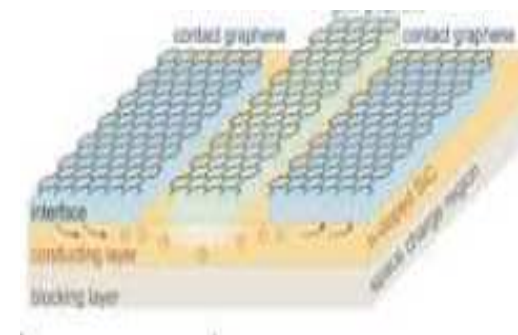
- **Aging/energy co-optimization via power management**



- CAD for beyond-CMOS technologies

- **Graphene devices**

- Delay and power modeling of devices based on p-n junctions based on polarized graphene
- Automated synthesis techniques for re-configurable gates based on these devices





Ongoing Projects

- Strong record of EU funded projects
 - 30+ in the last 10 years
- Not just strictly “technology” projects
 - Cross-domain application of EDA methods to
 - **Energy/smart grids**
 - **Automotive**
 - **Smart buildings/cities**
 - **Smart devices**

Funded research

- JU ENIAC

- ~~—MOTORBRAIN~~

- ~~—ERG~~

- ~~—E2SG~~

- ~~—IDEAS~~

- JU ARTEMIS

- ~~—IOE~~

- ~~—VETESS~~

- ~~—DEMANESS~~

- ARROWHEAD

- FP7/FET

- ~~—SMAC (IP)~~

- ~~—TOUCHMORE (STREP)~~

- CONTREX (IP)

- TRIBUTE (STREP)

- DIMMER (STREP)

- READY4SMARTCITIES (CSA)

- HUMAN BRAIN PROJECT (FET)

- ~~—LAB4MEMS (KET)~~

- GRAPHENE (FET)

Legend:

AUTOMOTIVE

ENERGY

SMART CITIES/BUILDING

TECHNOLOGY/CAD